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Attorney Docket No. 990566A1

AMENDMENT TO THE CLAIMS:

The listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

Claims 1-5 (Cancelled)

6. (Previously Presented) An encryption system, comprising:
a random number selector subsystem for generating random numbers from adjusted data bits of a Receive Automatic Gain Control circuit, wherein said adjusted data bits are generated from said Automatic Gain Control circuit operating on a received signal; and
an encryptor for encrypting a signal using said random numbers.
7. (Previously Presented) An encryption system, comprising:
a random number selector subsystem for generating random numbers from instantaneous variations of the DC offset component of the input signal, wherein said variations are generated from said DC Offset Correction Loop circuit operating on a received signal; and
an encryptor for encrypting a signal using said random numbers.
8. (Previously Presented) An encryption system, comprising:
a random number selector subsystem for generating random numbers from variations in the receive signal propagation delay over time, wherein a CDMA Time Tracking Loop circuit is operating to track said variations in the receive signal propagation delay over time; and
an encryptor for encrypting a signal using said random numbers.
9. (Cancelled)

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10. (Previously Presented) An apparatus for generating random data bits in wireless communications device, comprising:

means for processing a received signal from a Receive Automatic Gain Control Circuit;

means for adjusting data bits generated from said automatic gain control circuit; and

means for extracting said random data bits from said automatic gain control circuit.

11. (Previously Presented) An apparatus for generating random data bits in wireless communications device, comprising:

means for processing a received signal from a DC Offset Correction Loop;

means for generating random data bits from said DC Offset Correction Loop; and

means for extracting said random data bits from said DC Offset Correction Loop.

12. (Previously Presented) An apparatus for generating random data bits in wireless communications device, comprising:

means for processing a received signal from a Time Tracking Loop;

means for generating random data bits from said Time Tracking Loop; and

means for extracting said random data bits from said Time Tracking Loop.

Claims 13 – 33 (Cancelled)